

Greetings from Olaf Vogt

Director and Head of Application Marketing



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Director and Head of
Application Marketing

"I would like to thank all participants for joining this first-of-its-kind Nexperia virtual ESD seminar.

*The **automotive industry** is driven by the major trends of **electrification, autonomous driving and shared 'connected' mobility.***

*We also see that ever-increasing data rates, greater calculation power of System-on-Chips and IC miniaturization are making **systems even more sensitive to ESD.***

*With our Nexperia ESD Seminar, we want to **support the design community** in protecting applications and products against ESD issues and make systems more reliable.*

*Additionally, we published an **automotive ESD application handbook** to share our **expertise and best practices with you."***

ESD

APPLICATION HANDBOOK

AUTOMOTIVE EDITION

PROTECTION CONCEPTS, TESTING & SIMULATION FOR MODERN INTERFACES

Design Engineer's Guide



Nexperia ESD Seminar Session 2

ESD Protection for Automotive Interfaces

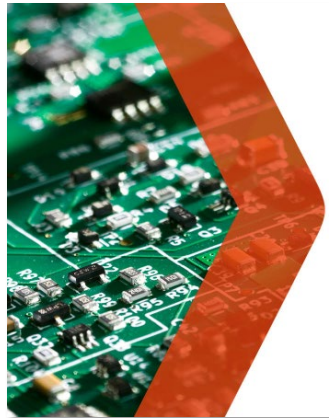
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Lukas Droemer

Product Manager Automotive ESD Protection & Filtering
lukas.droemer@nexperia.com

Recap | Nexperia ESD Seminar Session 1



nexperia

Nexperia ESD Seminar Session 1
Fundamentals of ESD Protection

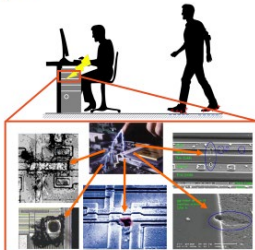
ESD – Electro Static Discharge

WHAT

A sudden discharge between persons, devices or components

HOW

- A charged person touches an integrated circuit (IC)
- A charged IC drops on a grounded metal plate
- A charged machine touches an IC



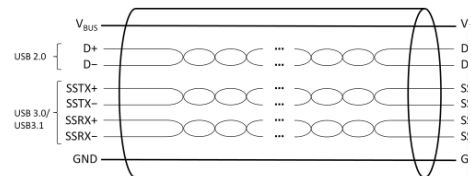
metallisation or PN junctions

X.

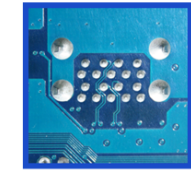
Selection Criterion

Selection Criteria: Number of signal lines

- Single signal line vs. multiple similar lines, e.g. USB 3.0



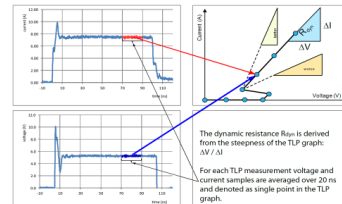
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ESD – Dynamic Resistance

Transmission Line Pulse

- Typical pulse width 100 ns
- VF-TLP (very fast) ~3 ns



The dynamic resistance R_{dyn} is derived from the steepness of the TLP graph: $\Delta V / \Delta I$
For each TLP measurement voltage and current samples are averaged over 20 ns and denoted as single point in the TLP graph.

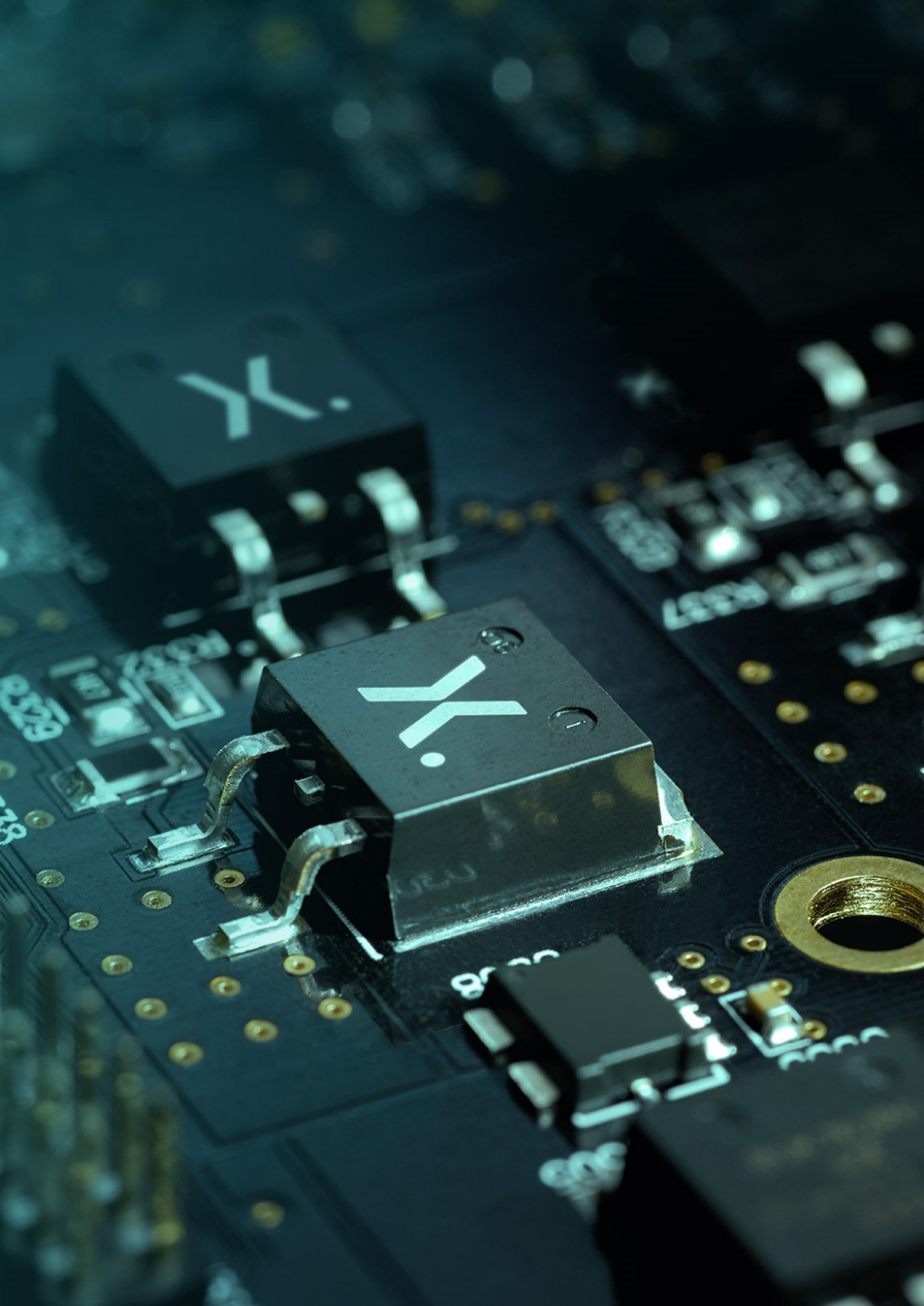
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X.

In session 1, we talked about:

- **Fundamentals of ESD Protection**
 - ESD protection background
 - Device vs. system level perspective
 - Impact and trends
- **Measurement and Characterization**
 - Selection Criterion
 - ESD Tolerance Test
 - Clamping Voltage
 - Dynamic Resistance using TLP
 - Zener and Snapback

In session 2, we will learn how to apply the concept of ESD protection with common automotive interfaces.

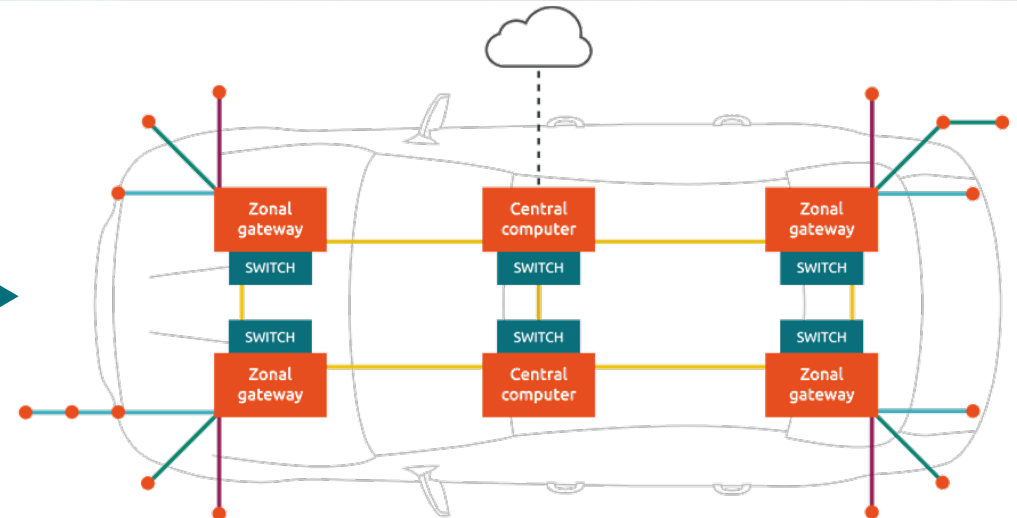
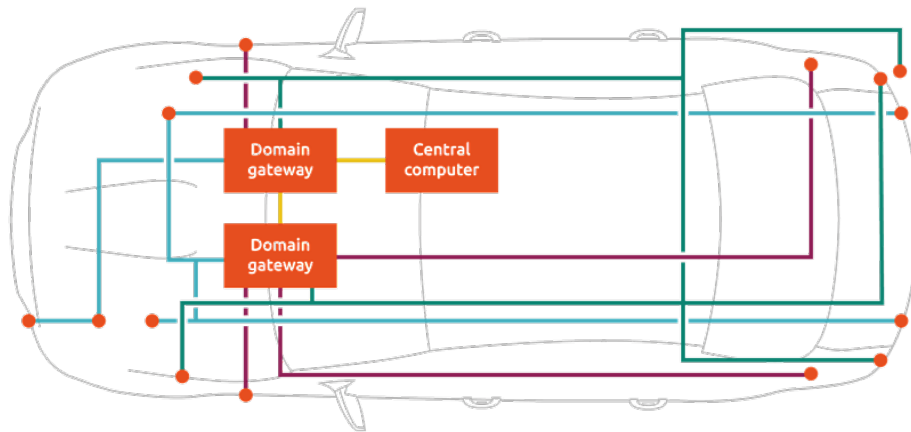


ESD Seminar Session 2

Agenda

- **Classic in-vehicle networks**
- **OPEN Alliance Ethernet**
- **SerDes**
- **Infotainment/Multimedia**
- **Extra: Package aspects**
- **Q&A**

Evolution of In-Vehicle Networking





ESD Seminar Session 2

Agenda

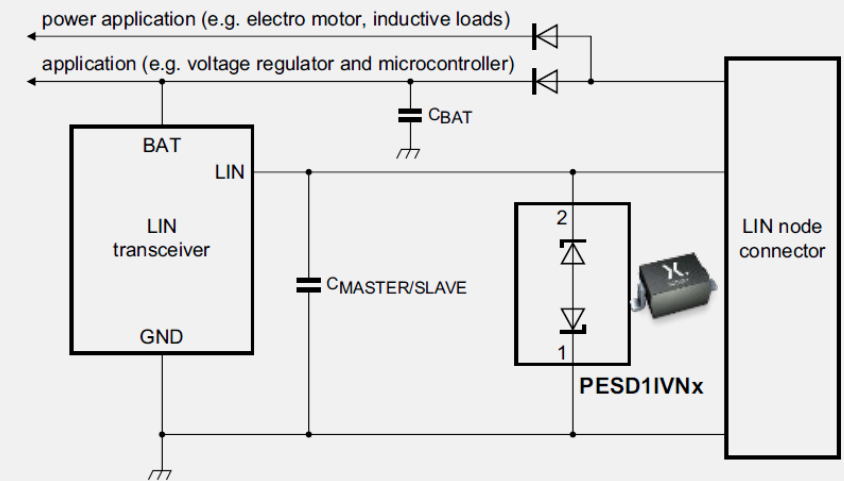
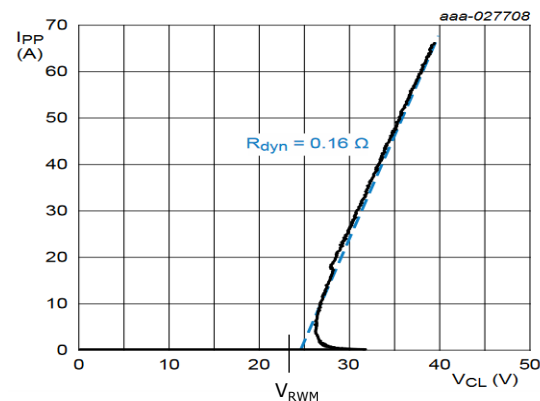
- **Classic in-vehicle networks**
- OPEN Alliance Ethernet
- SerDes
- Infotainment/Multimedia
- Extra: Package aspects
- Q&A

LIN

Local Interconnect Network



- Requirement for ESD protection depend on OEM
 - Sometimes only specific devices are approved, most of the time, just general requirements to the interface exist
- Common requirements:
 - Parasitic capacitance of 30-100pF max
 - Short-to-battery and jumpstart robust
 - ISO16750-2 (26 V) or internal norms (28 V)



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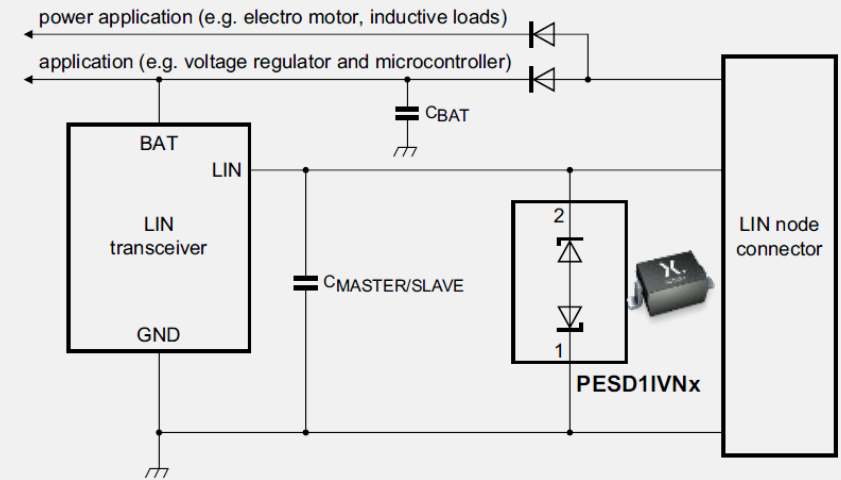
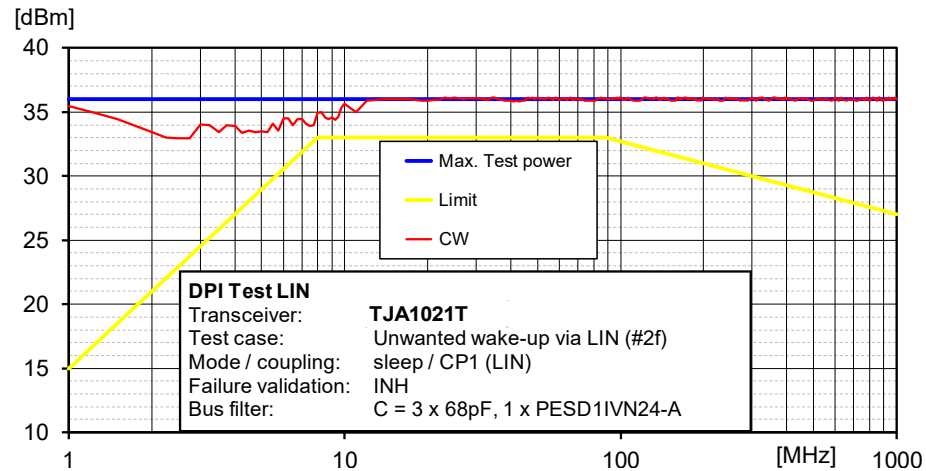
LIN

Local Interconnect Network



Compliance with ISO17987 in combination with a transceiver

- ESD
- RF emission
- immunity to transients -> ISO7637-2 (3a/b)
- RF immunity -> DPI



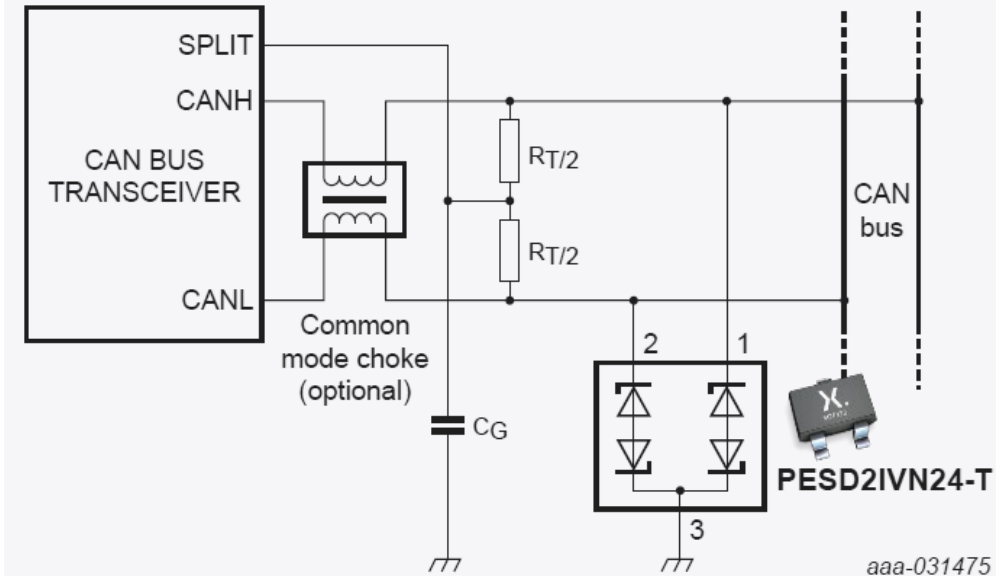
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CAN

Controller Area Network



- Requirement for ESD protection depend on OEM
 - Sometimes only specific devices are approved, most of the time, just general requirements to the interface exist
- Common requirements:
 - Parasitic capacitance of 17-30pF max
 - (for CAN FD 6-10)
 - Because of rather high C_p , matching is required
 - Short-to-battery and jumpstart robust
 - ISO16750-2 (26 V) or internal norms (28 V)



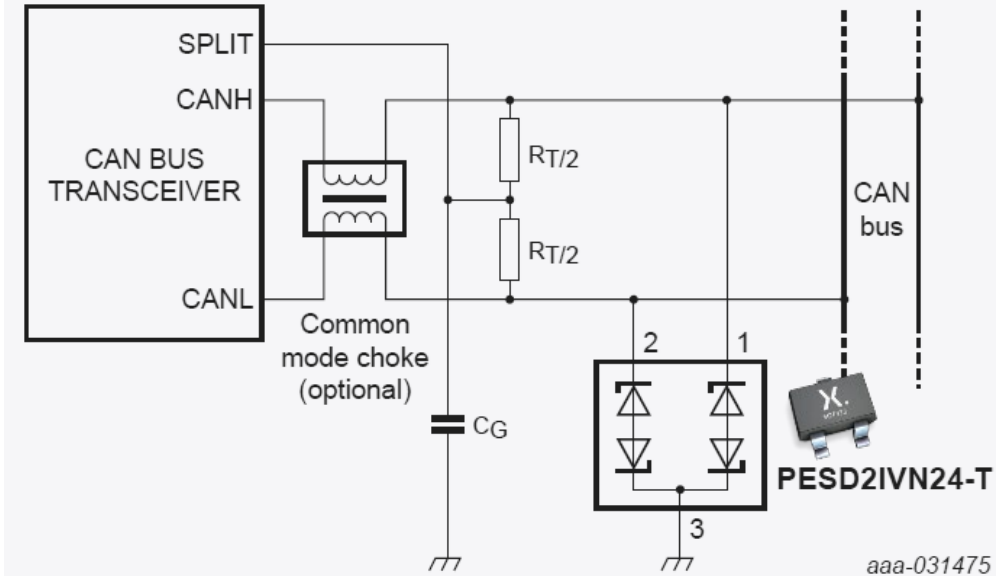
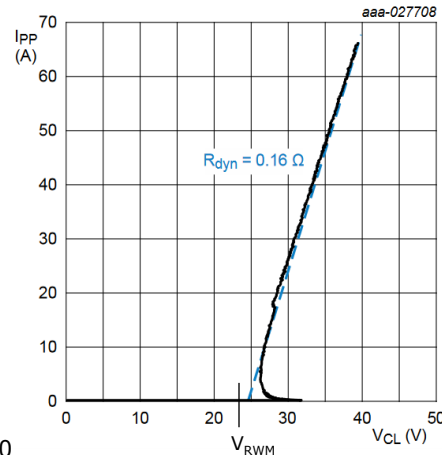
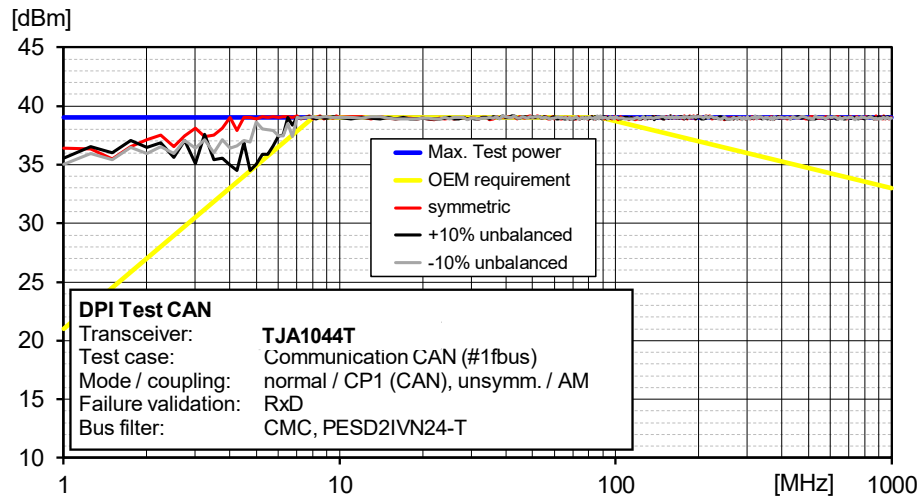
CAN

Controller Area Network

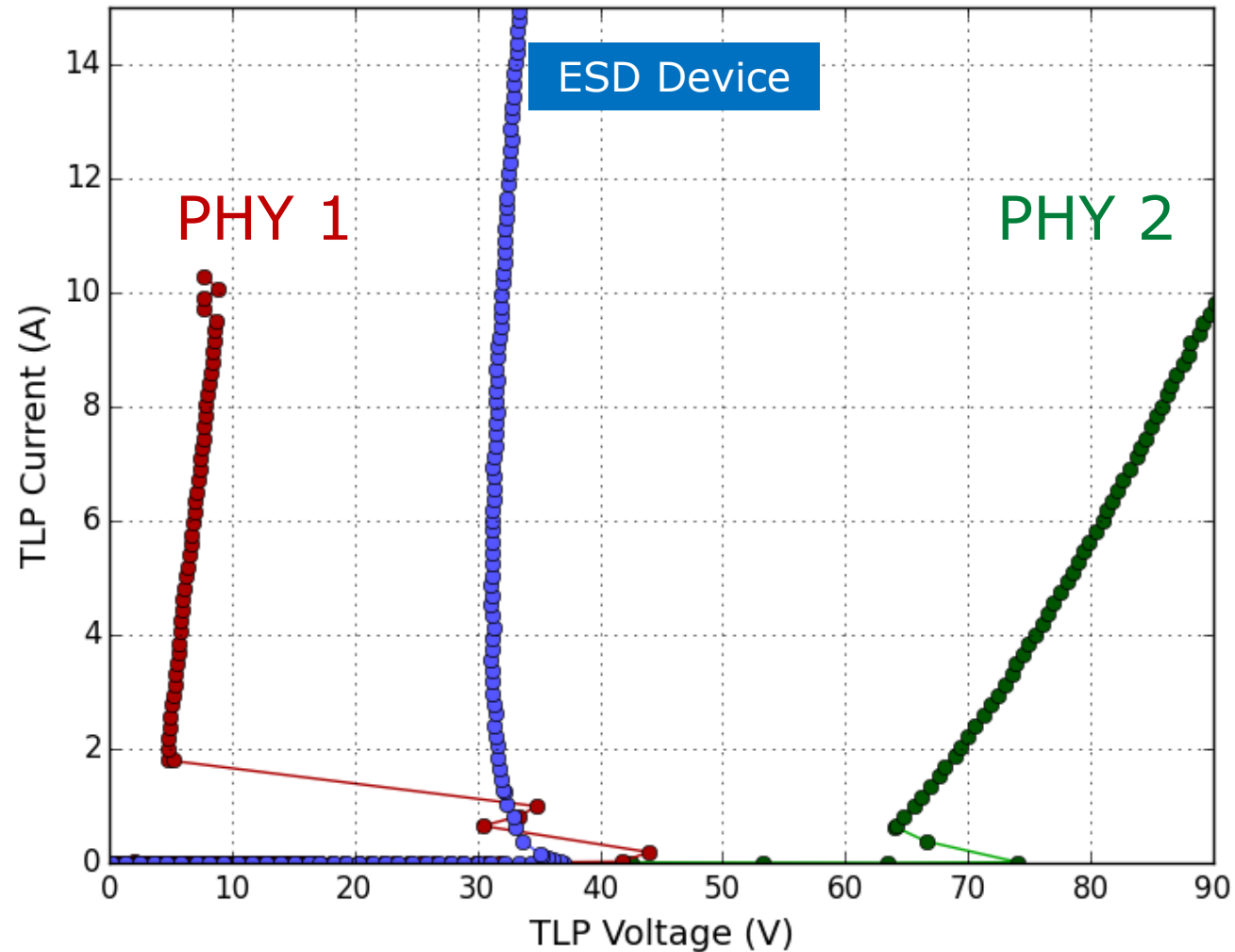


Compliance with IEC62228-3 in combination with a transceiver

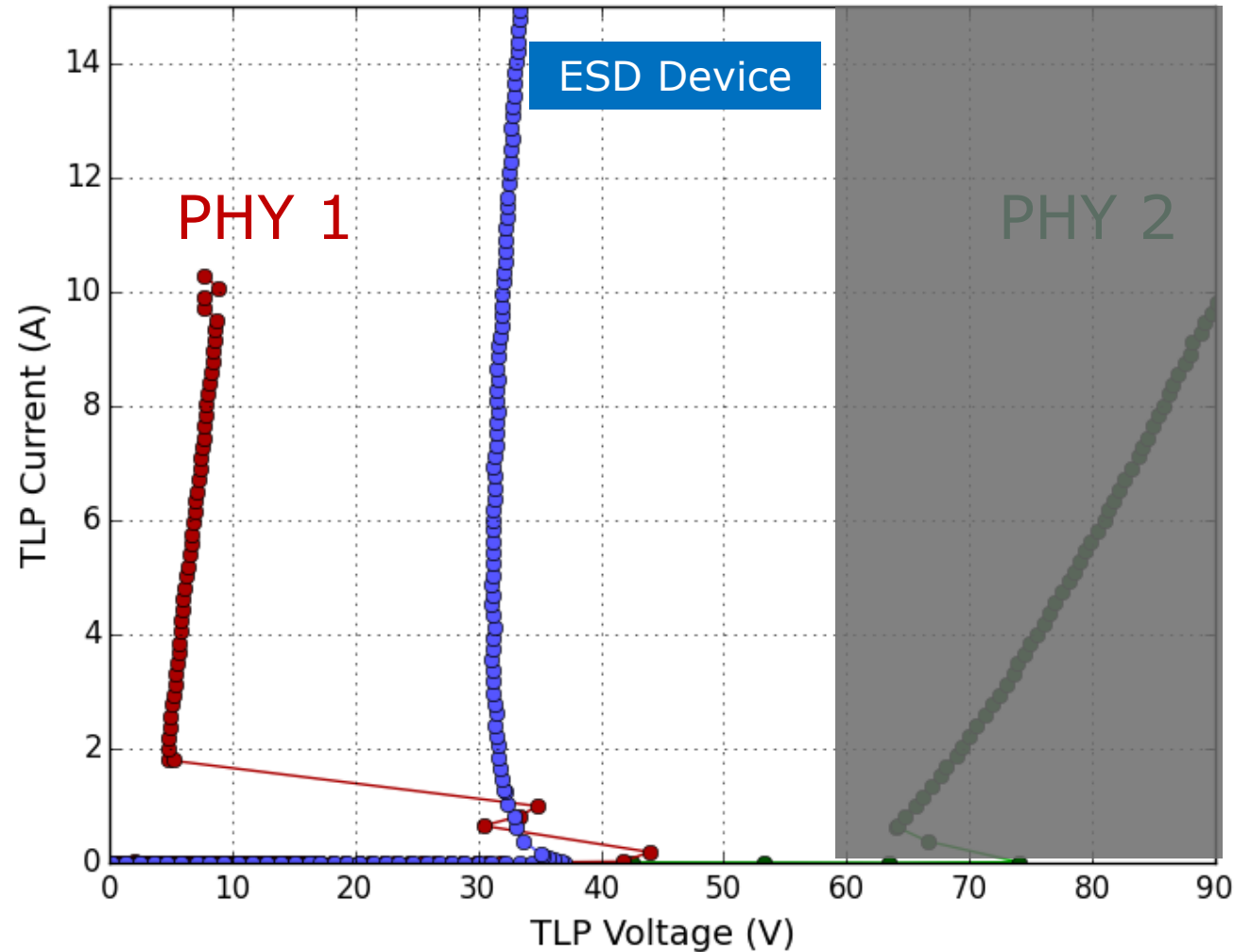
- ESD
- RF emission
- immunity to transients -> ISO7637-2 (3a/b)
- RF immunity -> DPI



Example CAN Device vs. System Robustness

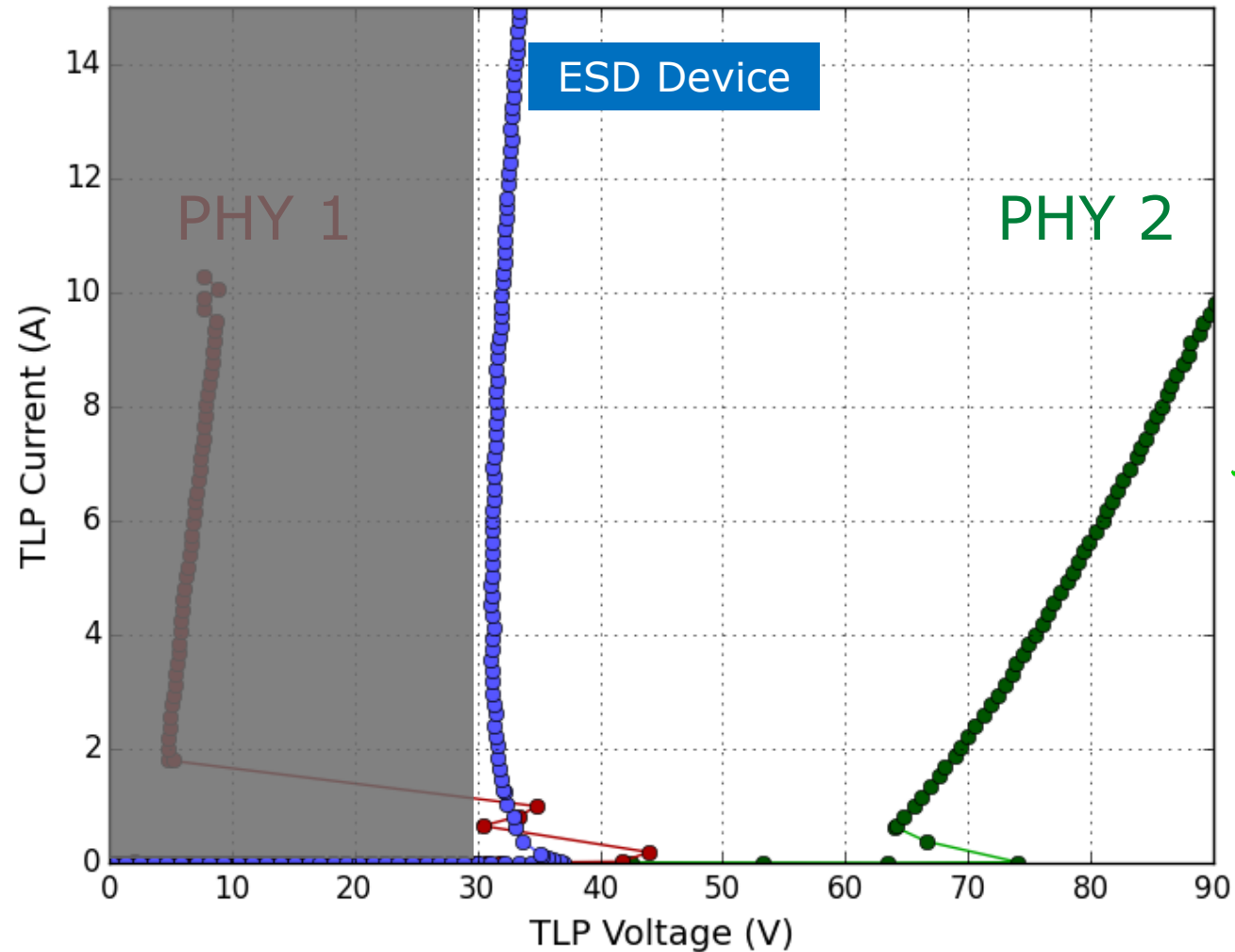


Example CAN Device vs. System Robustness

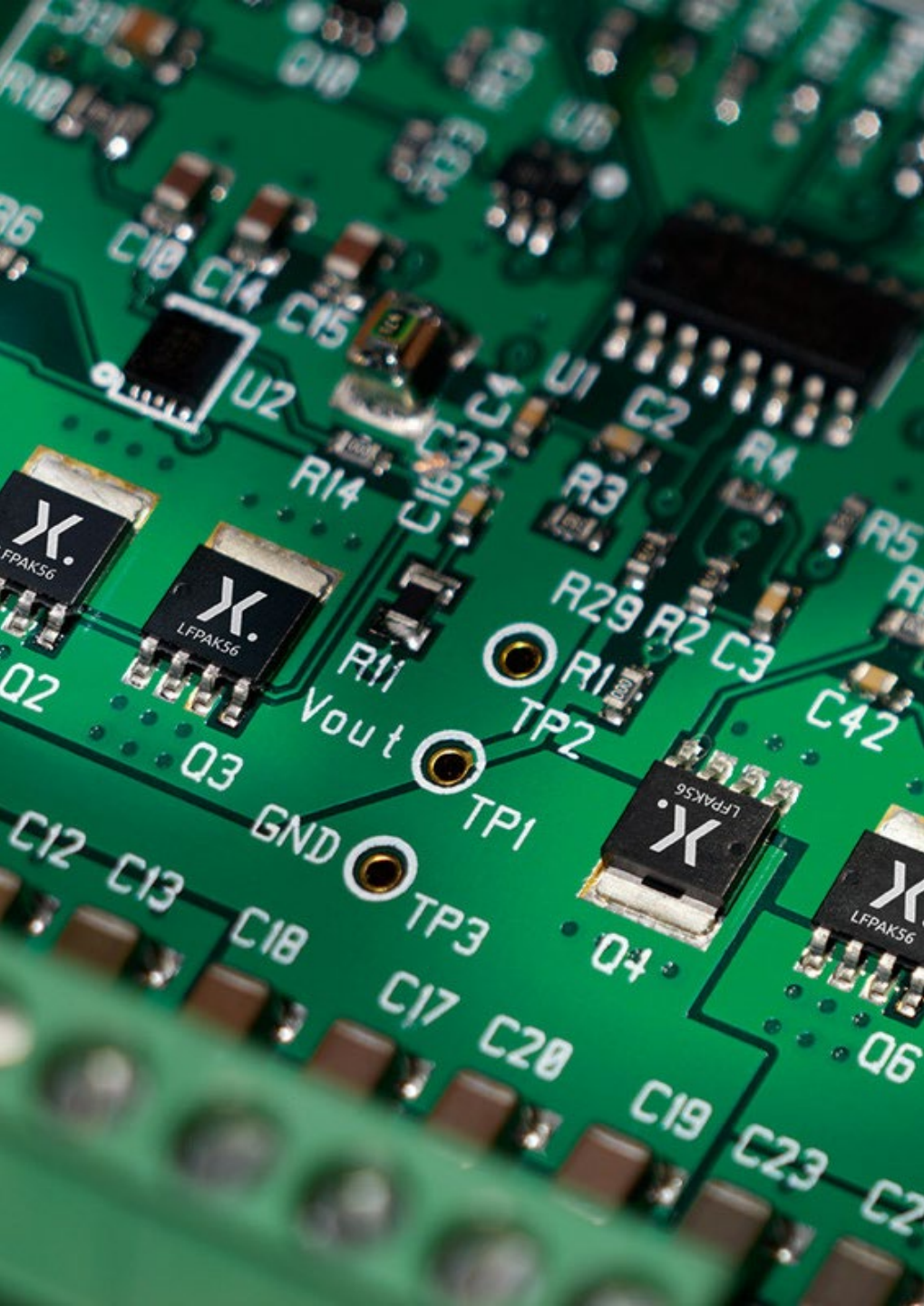


✗ PHY and ESD device not matched

Example CAN Device vs. System Robustness



✓ PHY and ESD device matched

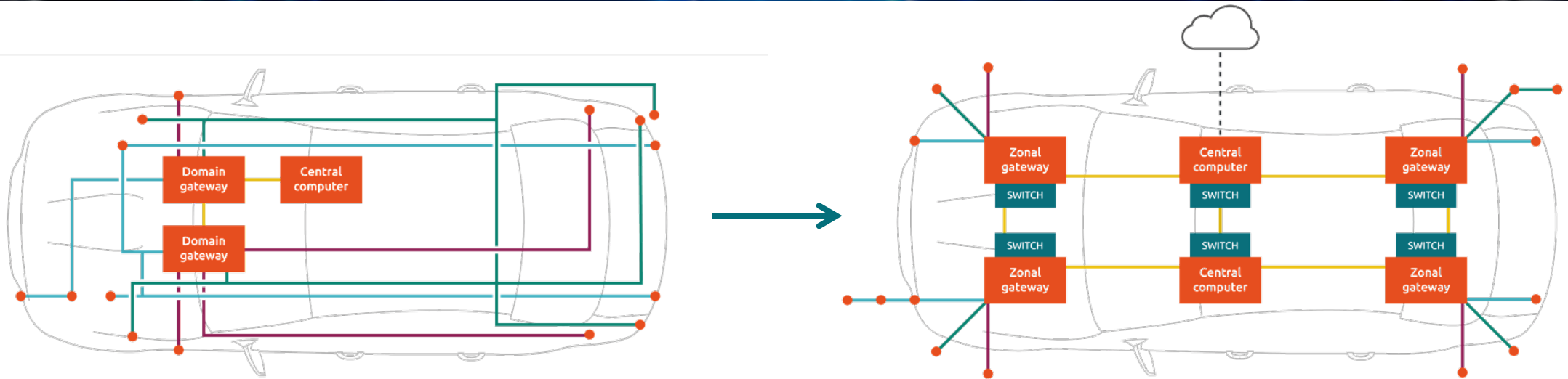


ESD Seminar Session 2

Agenda

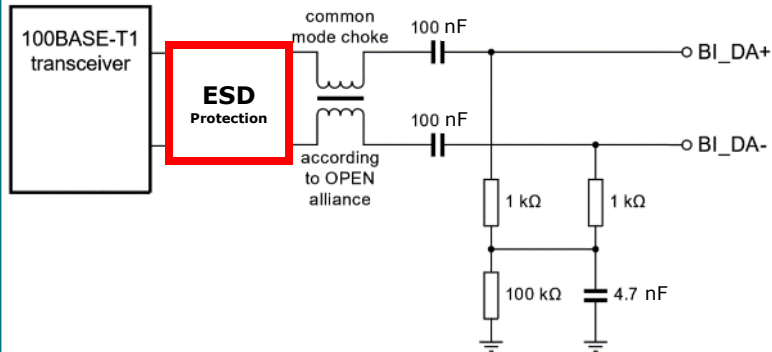
- Classic in-vehicle networks
- **OPEN Alliance Ethernet**
- SerDes
- Infotainment/Multimedia
- Extra: Package aspects
- Q&A

Automotive Ethernet requires the transition from traditional to domain and zonal architectures to support our shared vision of automotive future.



Classic Ethernet vs. OPEN Alliance

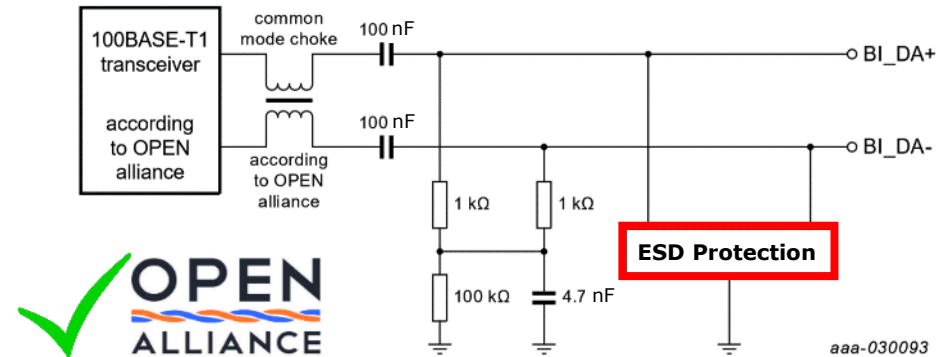
Classic Ethernet



ESD Protection behind DC block, CMC and in front of PHY

- **Dual sourcing of ESD protection critical**, because TLP curves and turn on behaviour need to match
- Internal protection of the PHY and external ESD protection cannot always be matched

OPEN Alliance Ethernet



ESD Protection in front of DC Block, CMC and PHY

- **Dual sourcing of ESD protection uncritical**, because ESD protection in front of DC block and CMC protect whole system
- External ESD protection is decoupled from internal protection of the PHY. **PESD2ETH1G-T matches with every PHY**

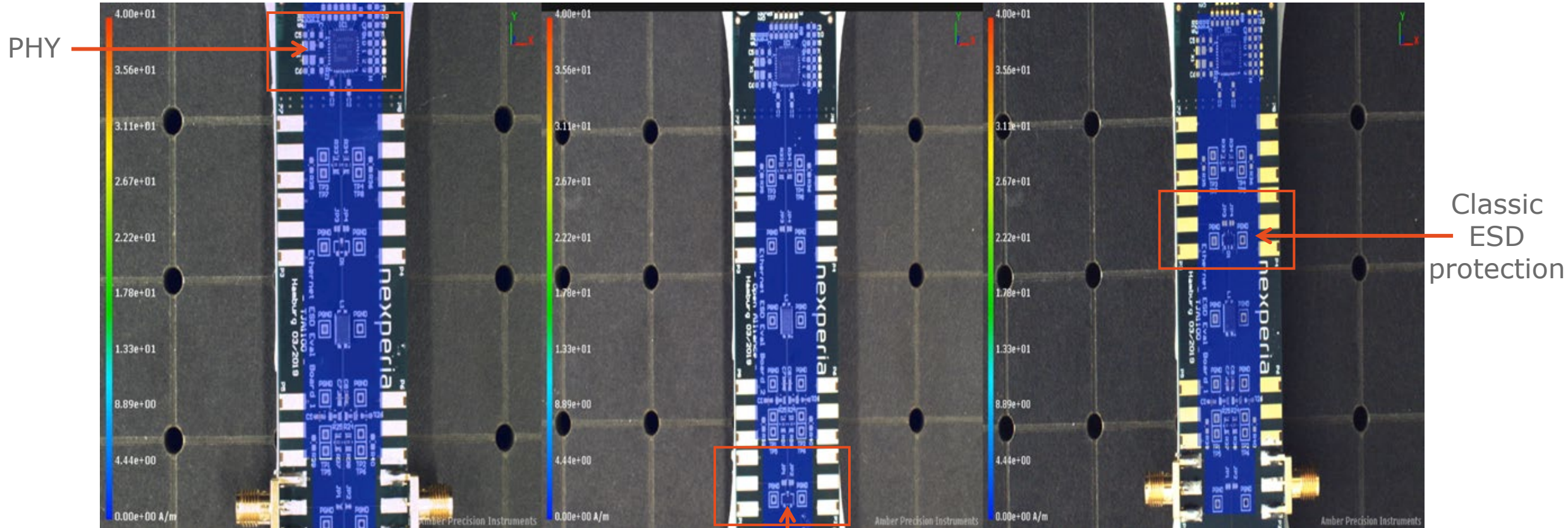
100BASE-T1 concept comparison

Comparison of ESD protection concepts evaluated by EMI scanner



No Protection

Classic Protection



PHY

Classic ESD protection

Connector

OA compliant ESD protection

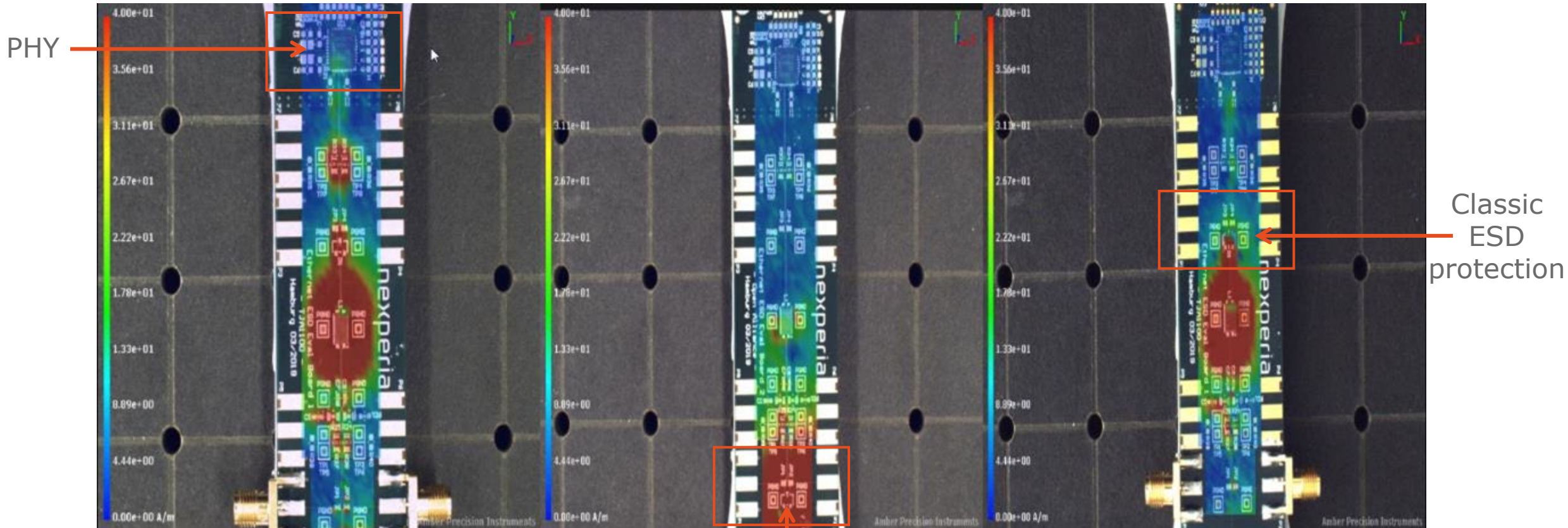
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Classic Protection



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Classic ESD protection

OPEN Alliance Spec. for ESD protection devices

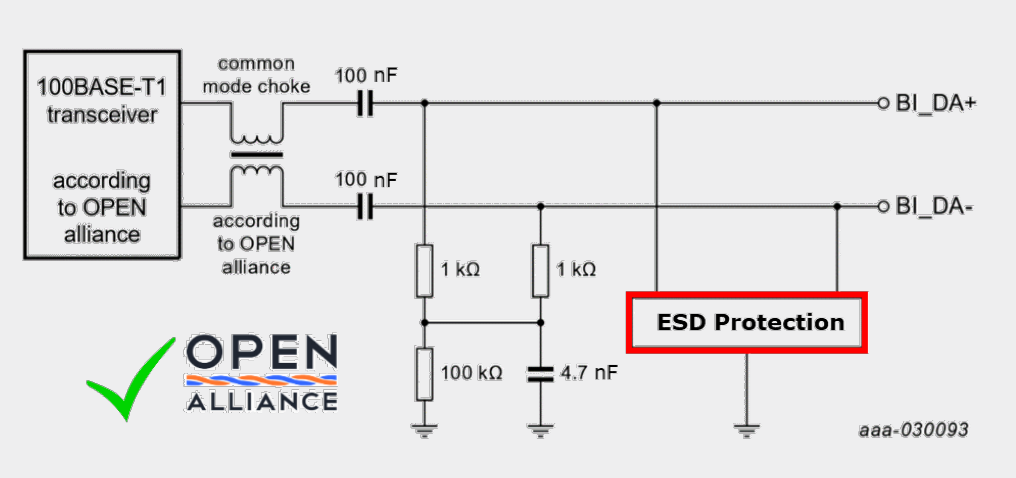
General requirements

- **General requirements**

- **Trigger voltage > 100V, $V_{DC,max} > 24V$**
- **Bi-direction device, 15kV IEC, 1000 discharges**

- **Additional tests**

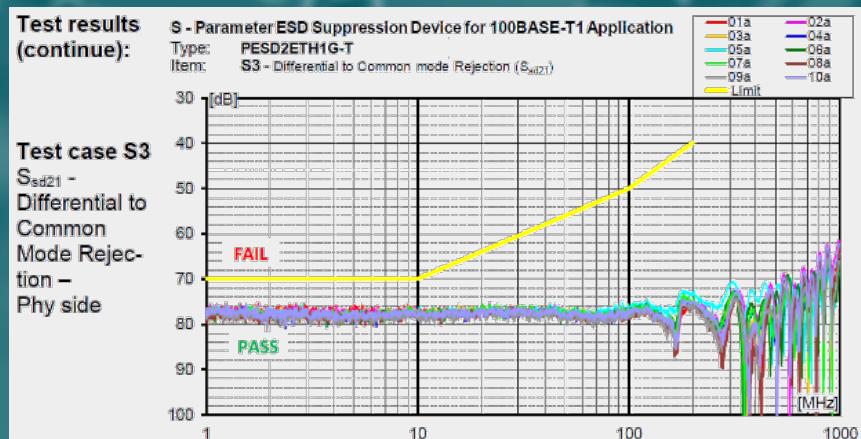
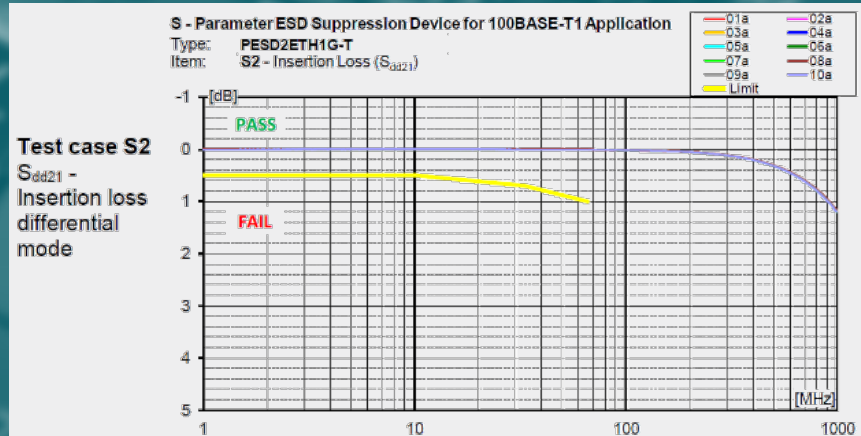
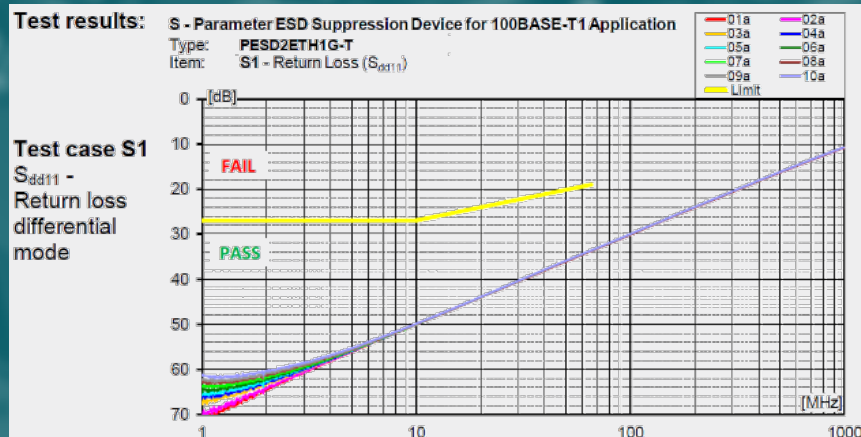
- Mixed mode S-parameter measurements
 - To evaluate transmission, symmetry, and mode conversion, replaces requirements on C_p and matching
- **Damage from ESD**
 - To verify degradation, first measure S-parameters, apply ESD (8kV) discharges, and check S-parameters again
- **ESD discharge current measurement**
 - Quantification of the current that would flow into the PHY
- **Unwanted clamping**
 - Evaluate impact of ESD device onto RF immunity testing



OPEN Alliance Spec. for ESD protection devices

Mixed mode S-parameter measurements

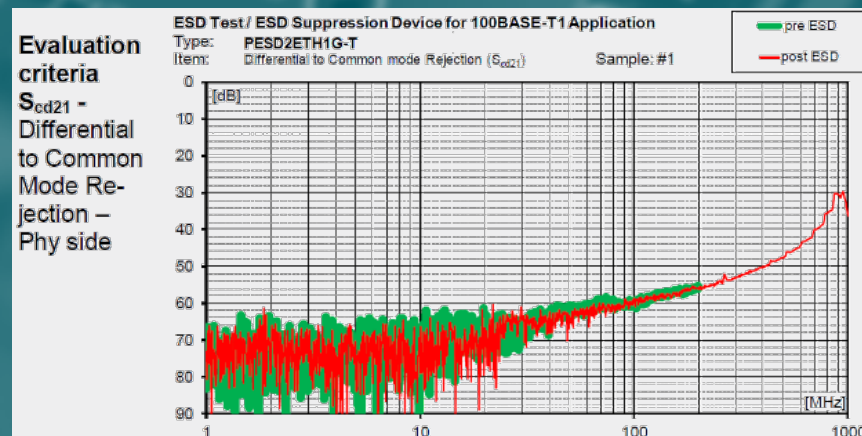
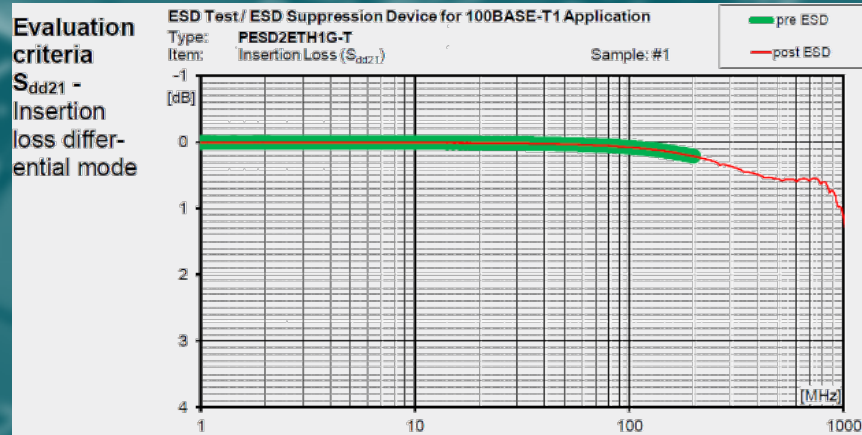
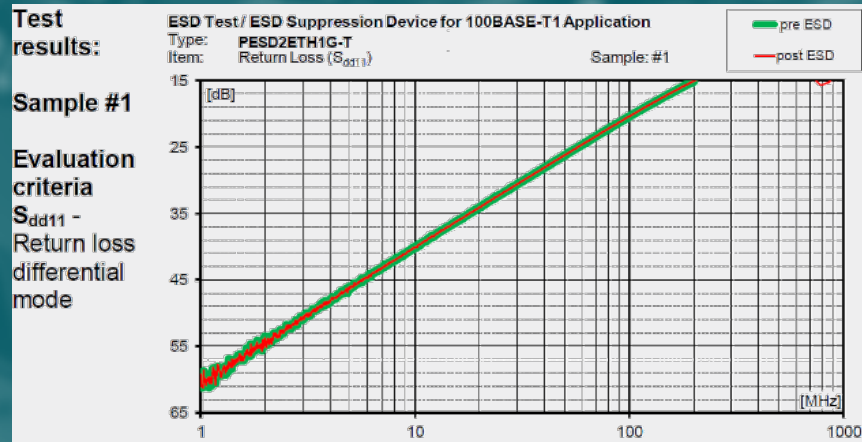
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OPEN Alliance Spec. for ESD protection devices

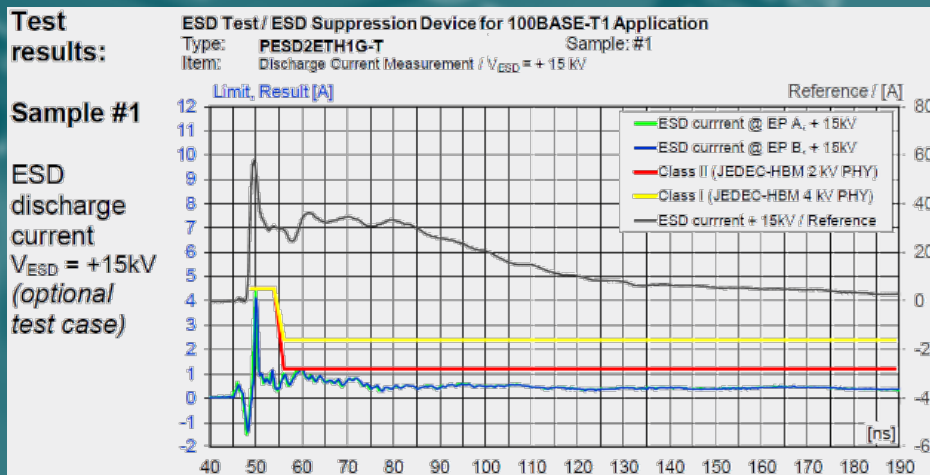
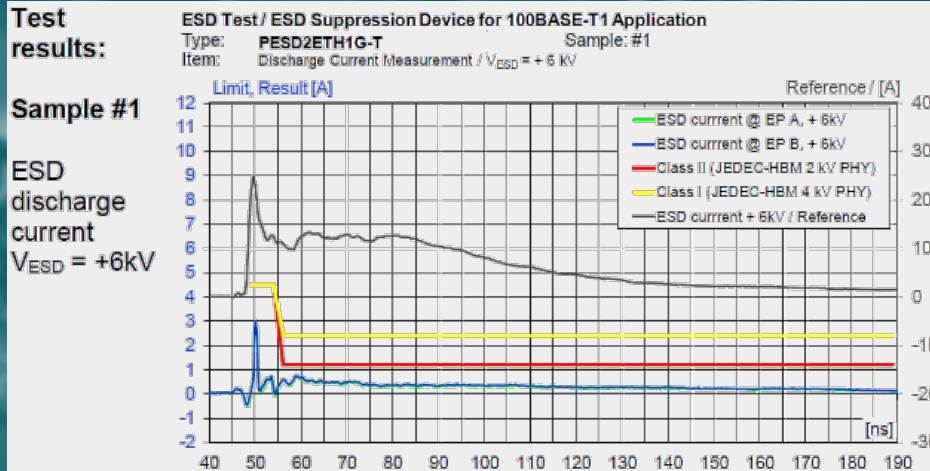
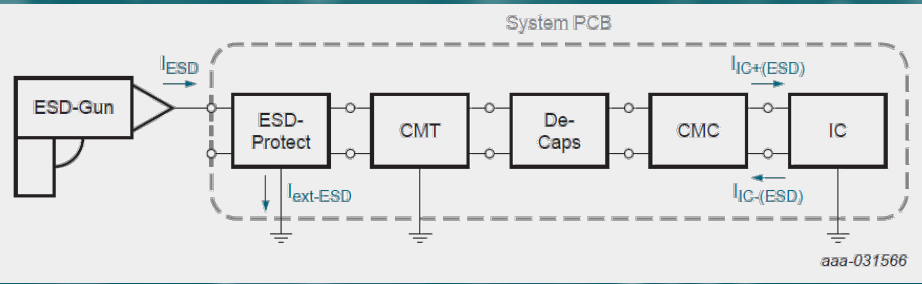
Damage from ESD

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OPEN Alliance Spec. for ESD protection devices

ESD discharge current measurement



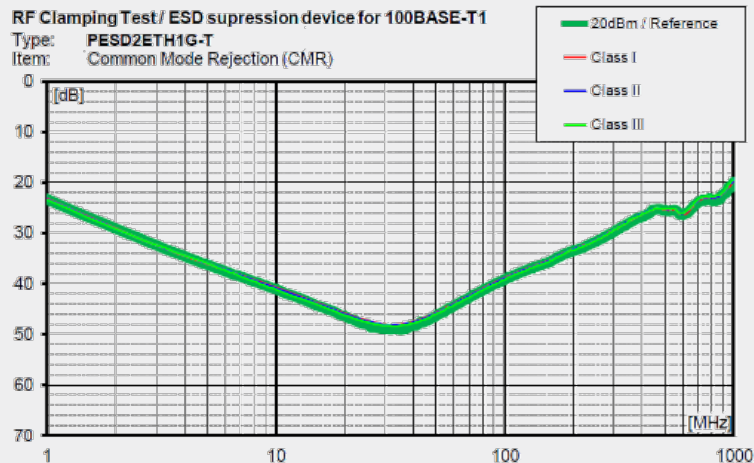
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OPEN Alliance Spec. for ESD protection devices

Unwanted clamping

Test results:

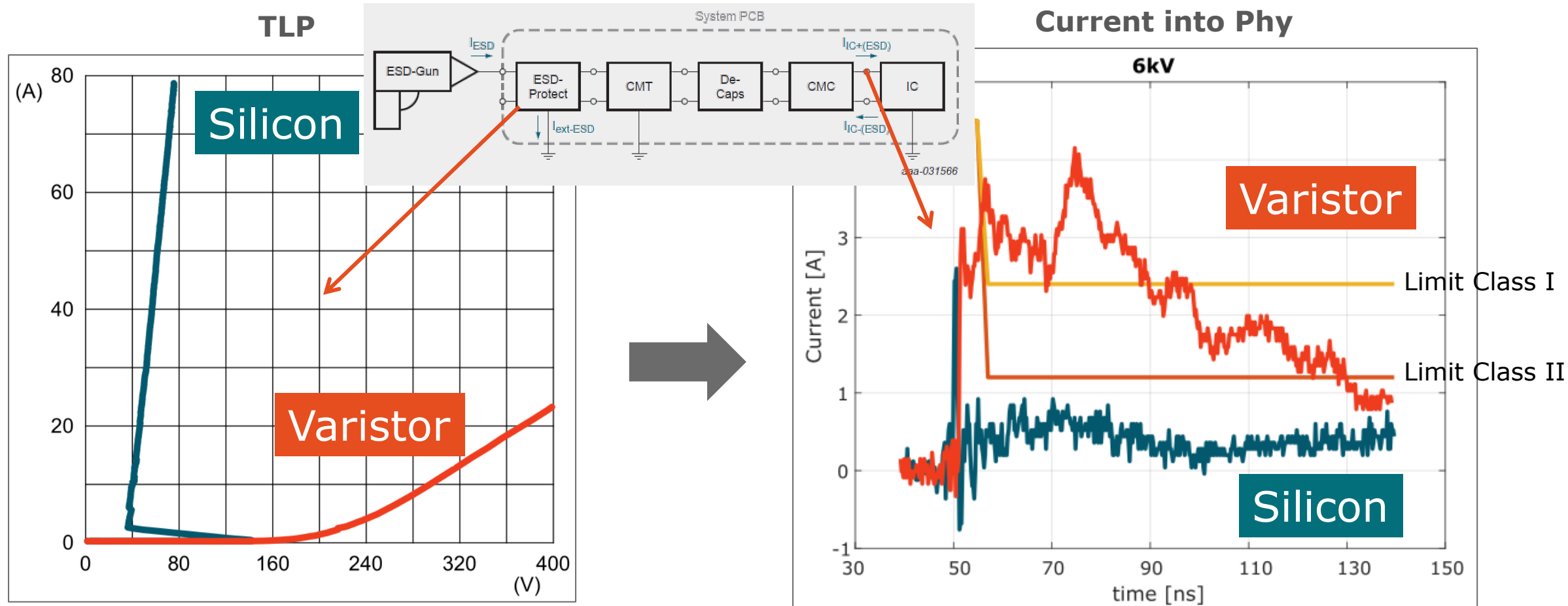
Evaluation criteria
CMC value
- Common mode rejection MDI test network

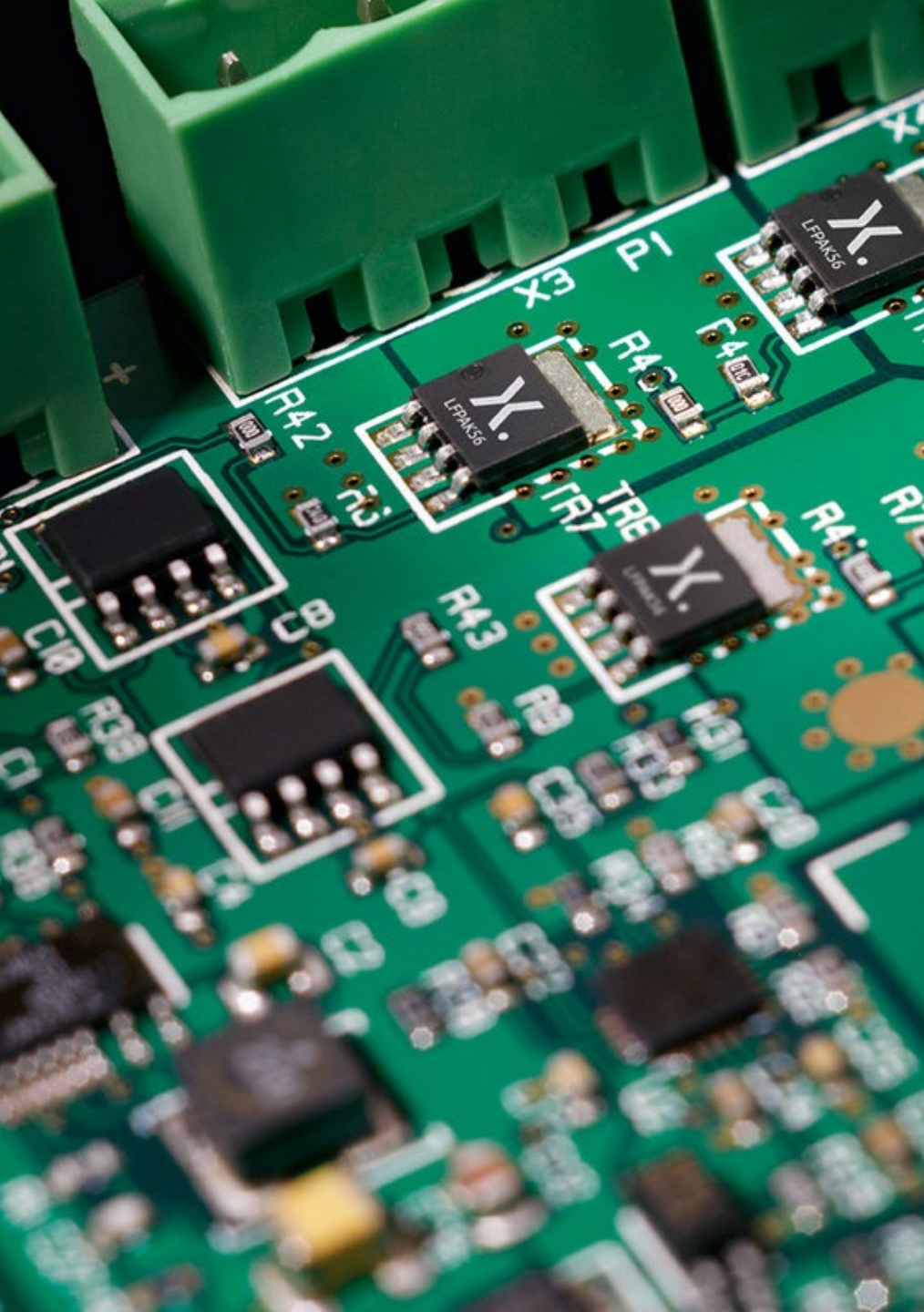


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ESD Discharge Current Measurement

Clamping performance and system robustness



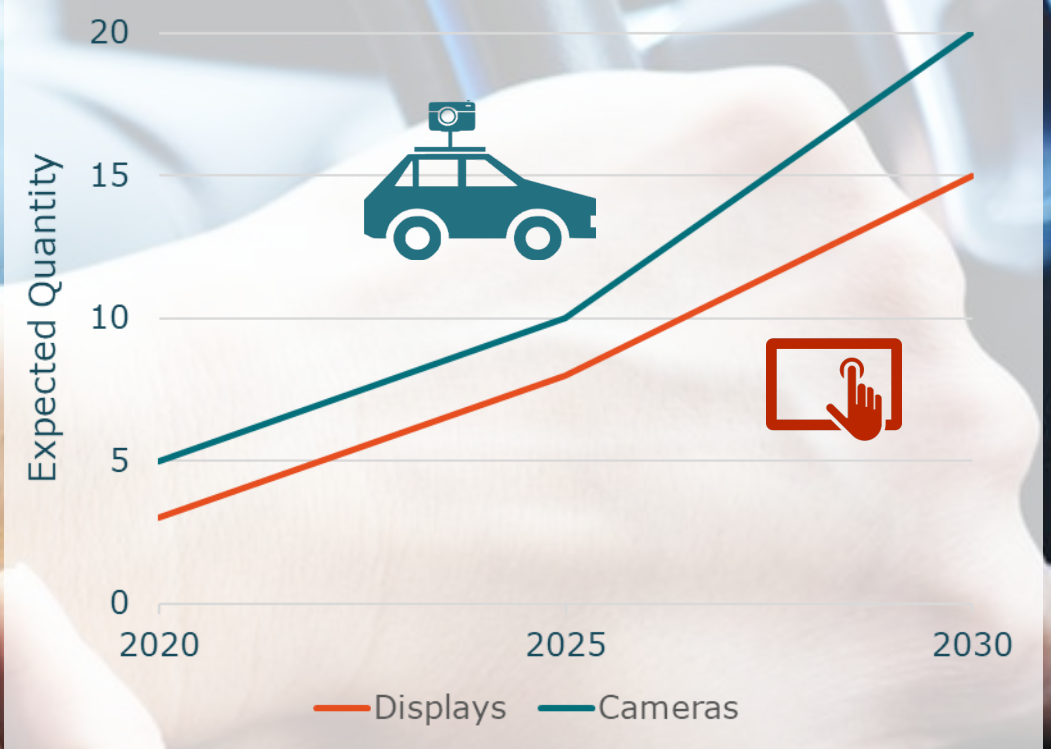


ESD Seminar Session 2

Agenda

- Classic in-vehicle networks
- OPEN Alliance Ethernet
- **SerDes**
- Infotainment/Multimedia
- Extra: Package aspects
- Q&A

SerDes



In 2019, **82% of vehicles** sold featured a touch screen, compared to 53% five years ago.

ALTEN Group

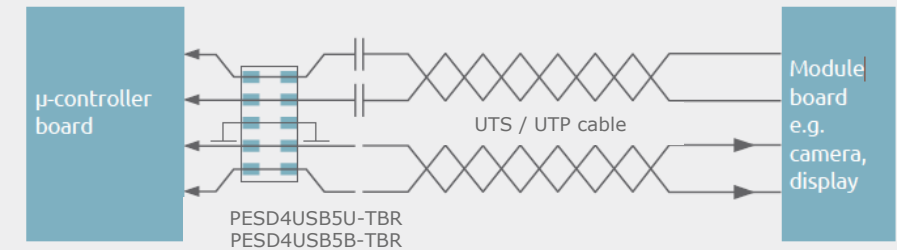
By 2030, the average car will have **~20 cameras** and **~15 displays per vehicle.**

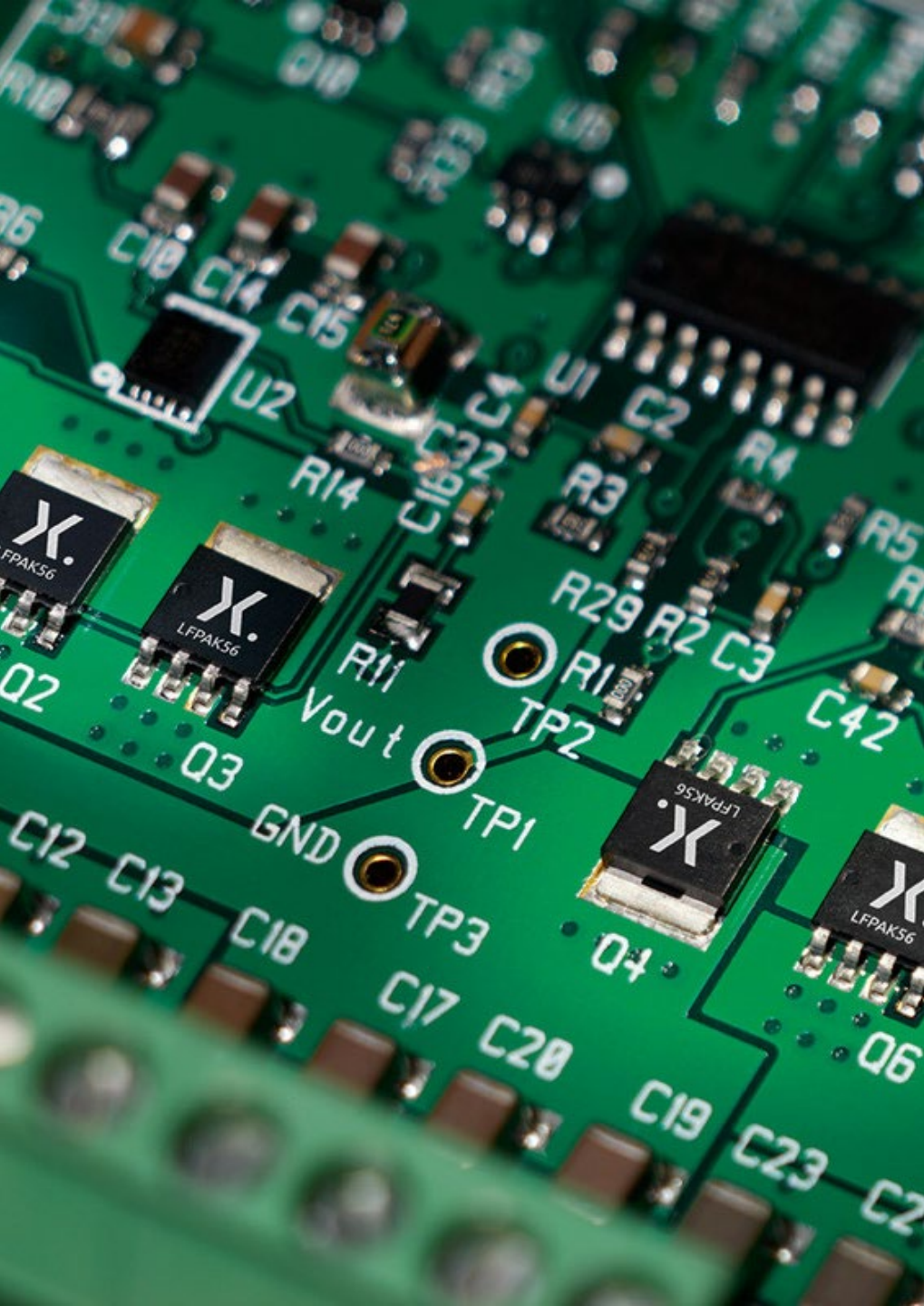
Brunner & Gauthier | ASA

SerDes

LVDS physical layer

- Many proprietary systems. No open standards and different handling by OEMs and IC vendors.
- Max parasitic capacitance depends on data rate
 - Usually, should be very low $C_p < 1\text{pF}$
 - Matching of lines usually not required, as C_p is very small
- Short-to-battery and jumpstart are not considered
- The lower the clamping voltage, the better is the protection.
- The protection should be as close as possible at the connector
- Leadless packages help to reduce parasitics





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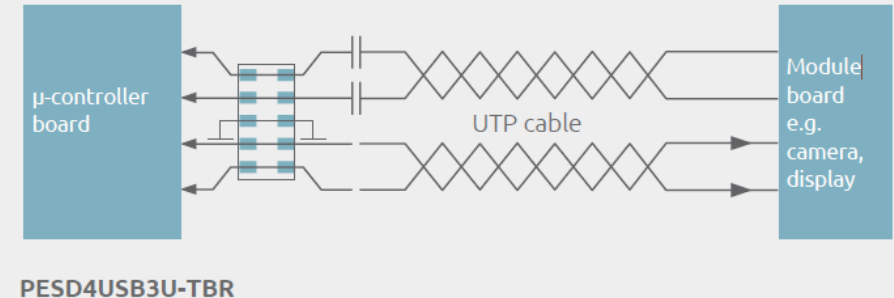
Infotainment

Common infotainment busses

USB - Universal Serial Bus

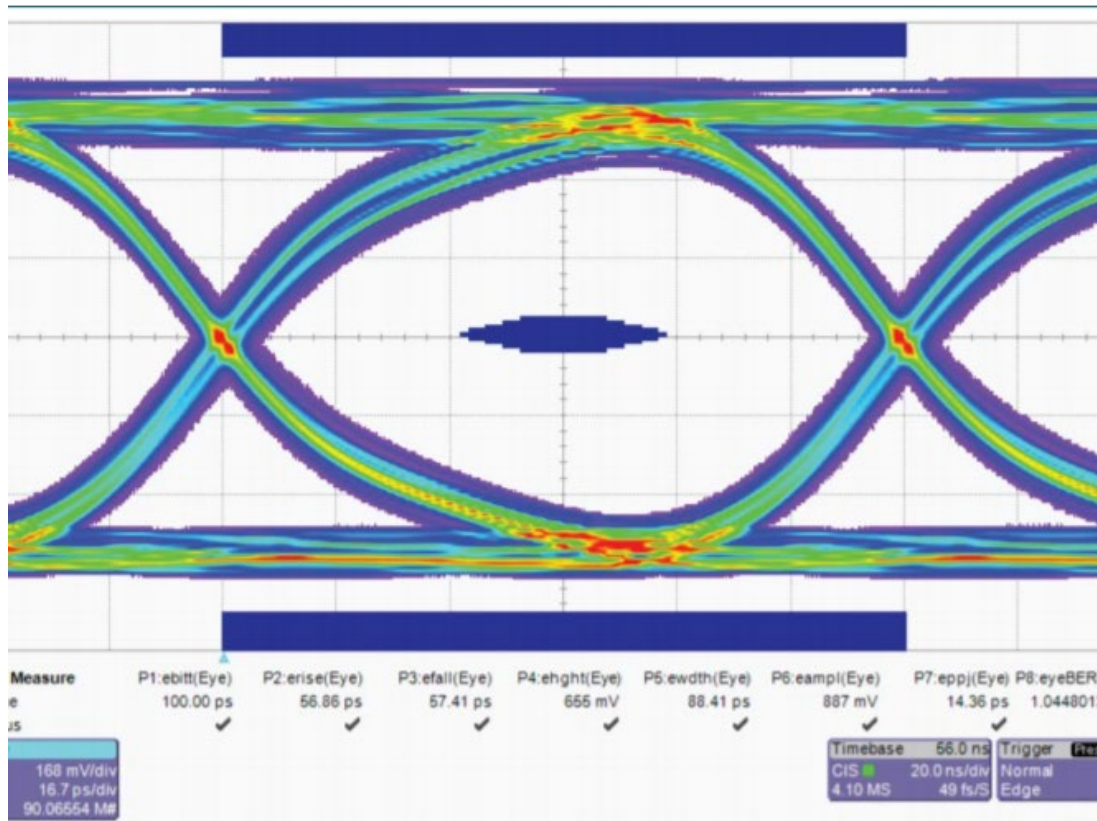
HDMI - High Definition Multimedia Interface

- Very low capacitance mandatory $\ll 1\text{pF}$
- RF compliance via
 - S-parameters
 - Eye diagrams
 - TDR
- Very low clamping required to protect sensitive ICs
- Fast turn-on required to prevent dynamic overshoot

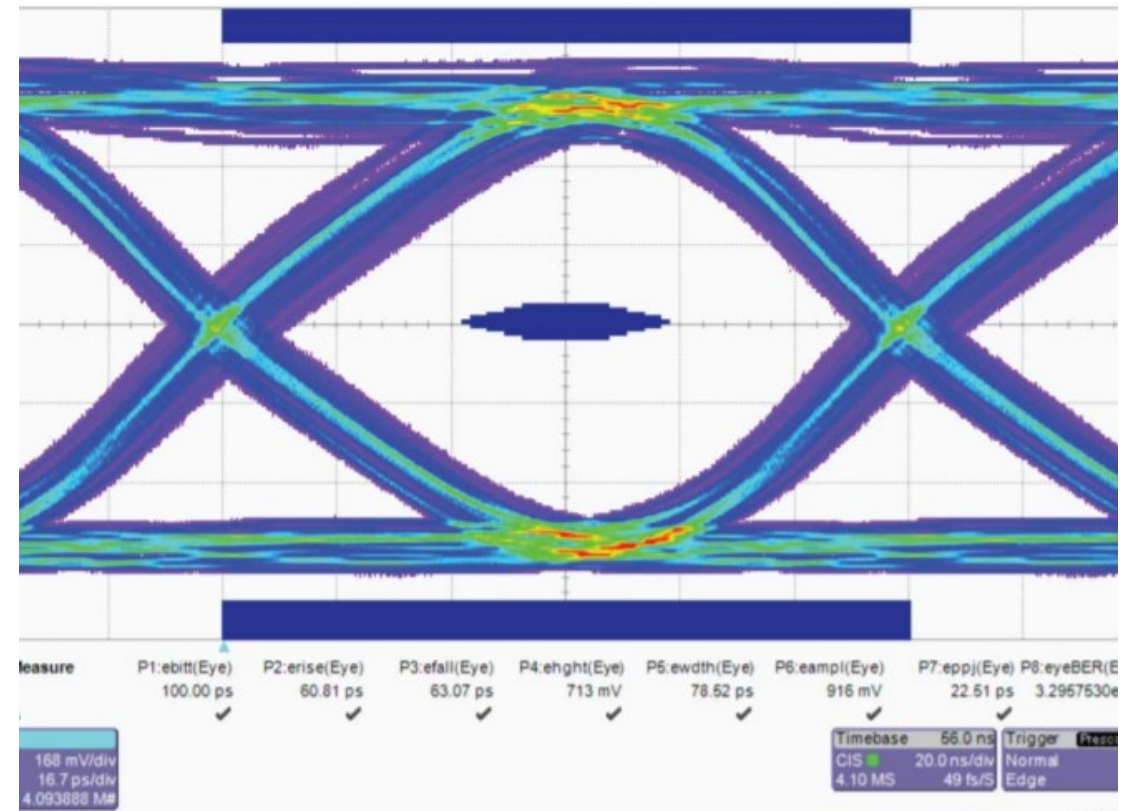


Signal integrity importance

Eye diagramm



w/o ESD device



with ESD device



ESD Seminar Session 2

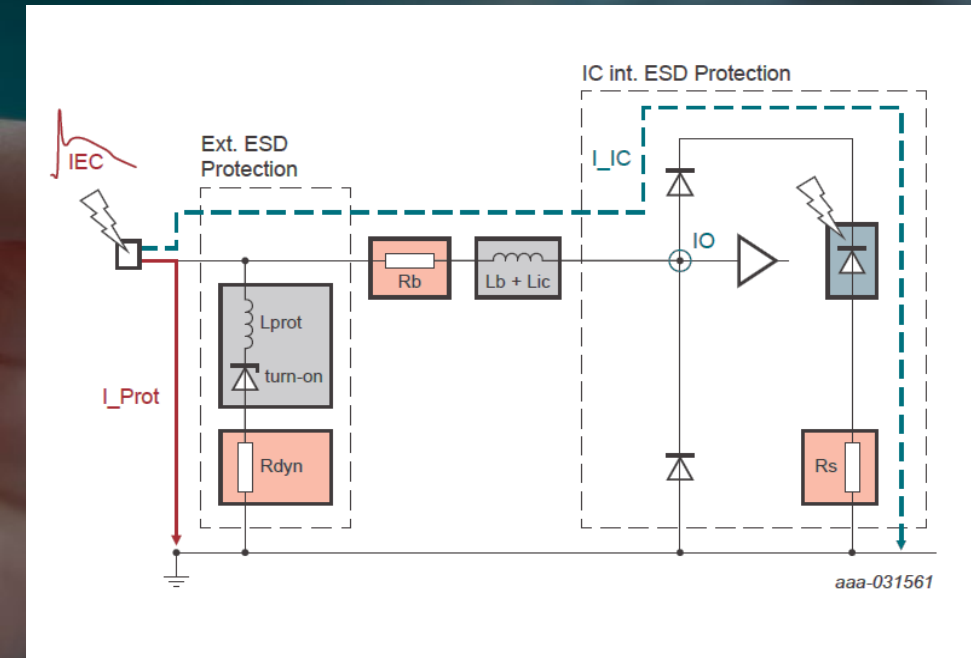
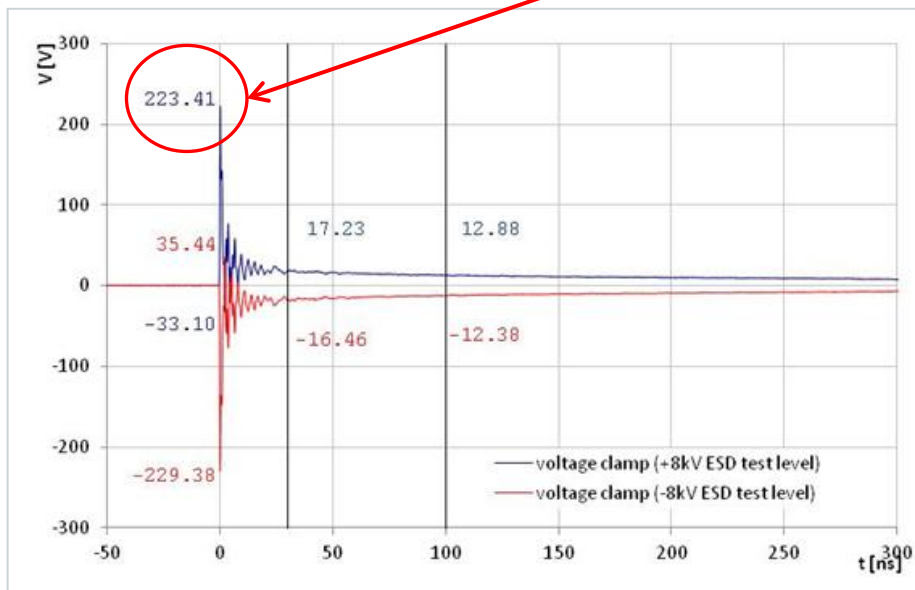
Agenda

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Package aspects – Clamping behavior

For high-speed busses

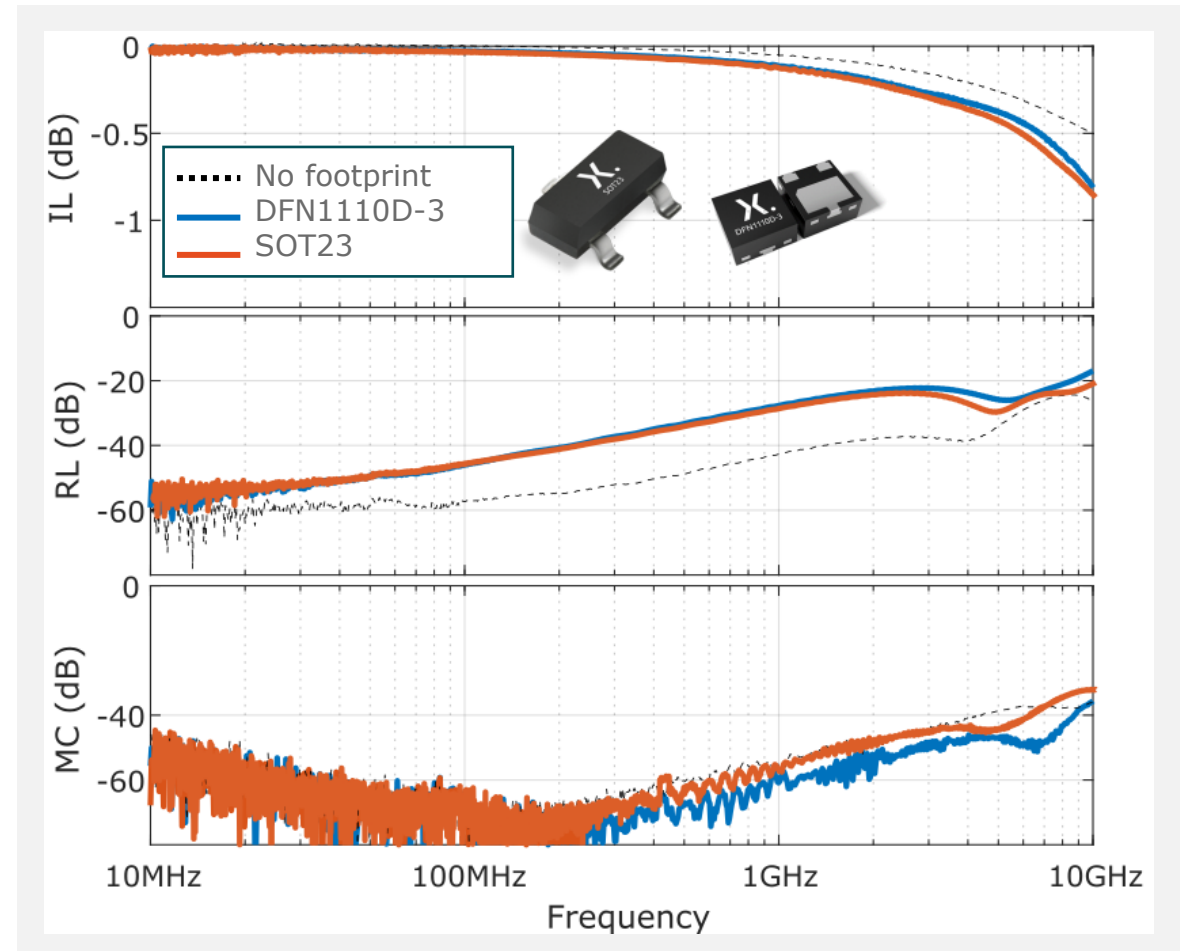
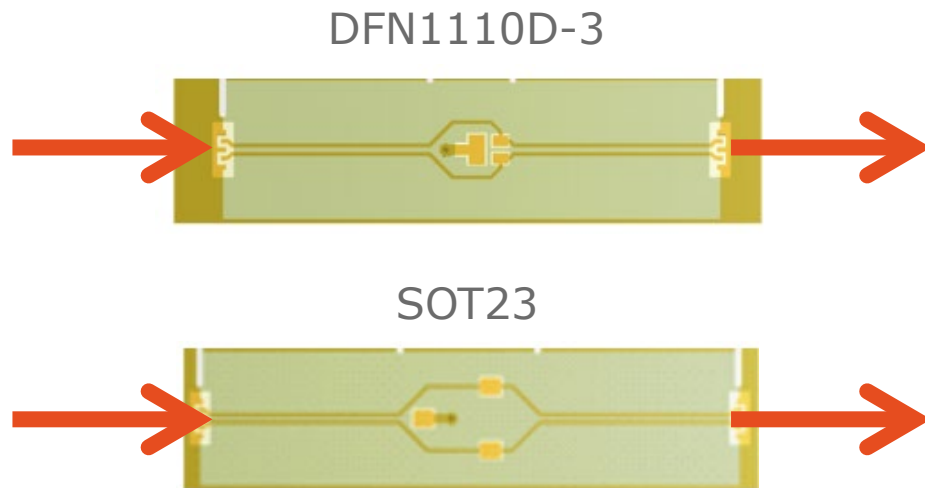
- R_{dyn} governs the clamping voltage in a quasi-static condition
- The dynamic behavior is determined by inductances and turn-on behavior



Package aspects – RF behavior

Comparison of SOT23 and DFN1110D-3 only footprint

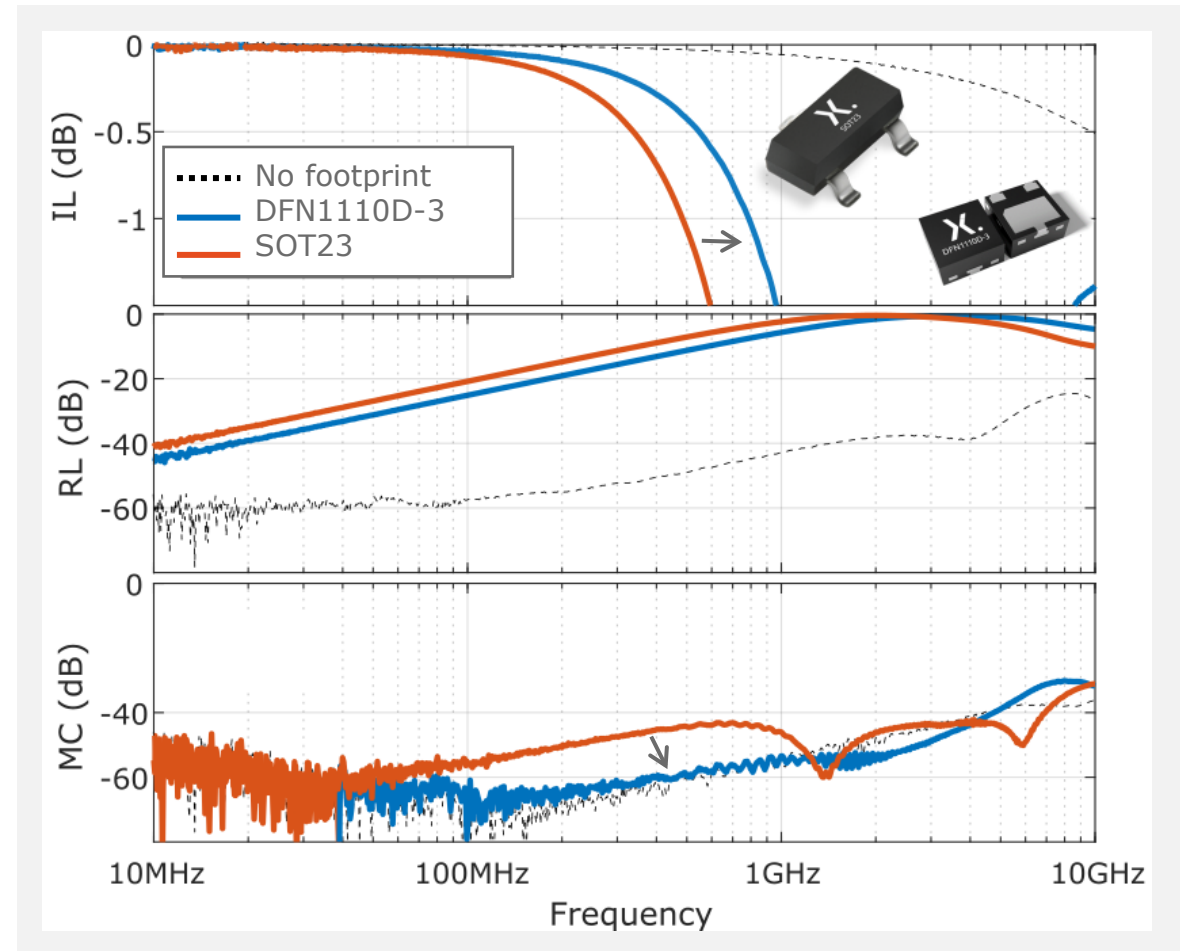
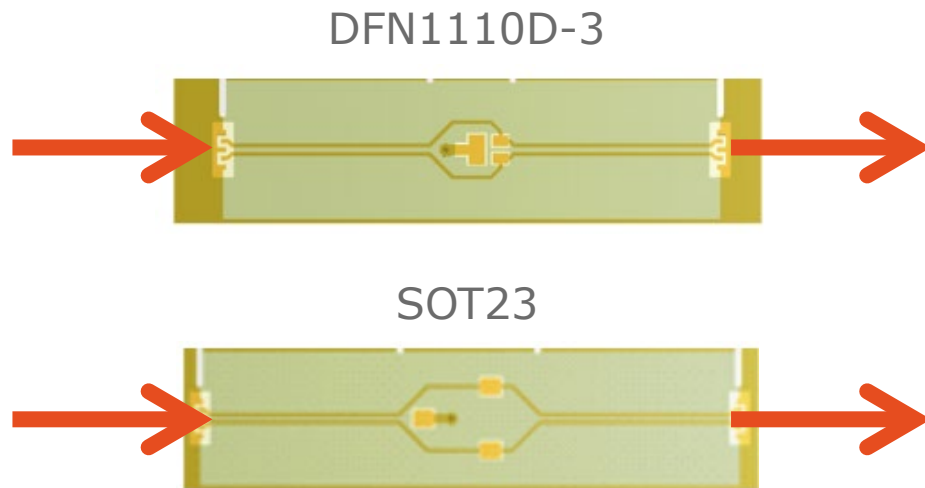
- Ca. 2 cm traces on FR4
- Dashed line: no footprint
- **No big difference between footprints**



Package aspects – RF behavior

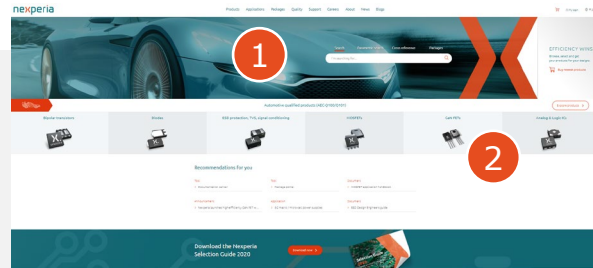
Comparison of SOT23 and DFN1110D-3 with PESD2CANFD24V ($C_p = 5.2$ pF)

- Ca. 2 cm traces on FR4
- Dashed line: no footprint
- **Clear advantage of leadless package**



Service & Support

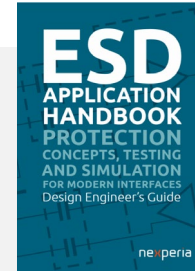
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Documentation Center
[Link Selection Guide](#)



Application Handbook
[Link ESD](#)



Lab support

- 1. Search Function
- 1. Cross Reference
- 1. Parametric Search
- 1. Package Search
- 2. Product Overview
[Path to Datasheets, Product Brochures, Application Examples, ...]

- Overview on all our Discrete, Logic and MOSFET devices
 - Diodes & Transistors
 - **Protection & Filtering**
 - MOSFETs
 - Logic
 - Packages

- ESD Fundamentals
- Measurements & Characterization
- Interfaces and applications
- English & Chinese version
- PDF & Hardcopy

Automotive version available now!

- Dedicated engineering team to support customer requests
- Solution investigation with various analysis tools especially
- TLP, EMI scan, SEED
- Contact us for details

Q&A

ESD for Electronic Design Engineers Seminar

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EFFICIENCY WINS.





EFFICIENCY WINS.